

The Eurasia Proceedings of Science, Technology, Engineering and Mathematics (EPSTEM), 2025

Volume 38, Pages 688-698

IConTES 2025: International Conference on Technology, Engineering and Science

Voltage Total Harmonic Distortion Reduction in Multi-level Single-Phase Inverters using the SHE-PWM Technique with a TLBO Algorithm

Smail Toufik

Solar Equipements Development Unit (UDES)

Boulouma Sabri

Solar Equipements Development Unit (UDES)

Seddaoui Naoual

Solar Equipements Development Unit (UDES)

Rahmani Hachemi

Solar Equipements Development Unit (UDES)

Abstract: This paper presents a modulation method based on the selective harmonic elimination-pulse width modulation (SHE-PWM) technique. The aim is to determine the optimal switching angles (SA) using the teaching learning-based optimization algorithm (TLBOA), capable of reducing the voltage total harmonic distortion (THD) present in the output waveform of single-phase multi-level inverters (MLIs). The performance of proposed technique has been verified through simulation using Matlab script and Simulink environment. The TLBOA-based SHE-PWM technique is applied to a 17-level inverter to eliminate 5th, 7th, 11th, 13th, 17th, 19th and 23rd harmonics, achieving an improvement of up to 4.31% in the phase voltage THD content in the range of modulation index (MI) 0.5 to 0.95. Furthermore, the superiority of the proposed technique was confirmed by comparison with conventional modulation techniques, which showed that the proposed technique produces lower THD than those used in the equal phase (EP), half-equal phase (HEP), feed forward (FF) and half height (HH) methods.

Keywords: SHE-PWM technique, TLBO algorithm, Total harmonic distortion, Conventional modulation techniques

Introduction

In recent years, multi-level inverters (MLIs) have been extensively employed in renewable energy applications and diverse industrial sectors, providing numerous benefits compared to traditional two-level inverters (TLIs). They have received an increasing interest from people in research and academia. In the research literature, there are three basic MLIs configuration: Diode-Clamped MLI (DC-MLI) (Adam et al., 2012), Flying Clamped MLI (CC-MLI) (Meynard et al., 1992) and Cascaded H-Bridge MLI (CHB-MLI) (Chunyan et al., 2015). Compared to TLIs, MLIs have many advantages including, lower electromagnetic interference (EMI), minimizing (dv/dt) across switches and suitability for medium or high voltage high power applications such as wind turbines (WT), photovoltaic systems (PVS) and electric vehicles (EV) (Charan et al., 2015; Sedaghati et al., 2023; Elias et al., 2022; Taiea et al., 2019; Pires et al., 2017; Sotoodeh et al., 2013; Mehta et al., 2022; Meraj et al., 2023).

To enhance the efficiency and performance of MLIs, numerous modulation techniques, new topologies and optimal operating strategies have been reported in the literature (Behera et al., 2022), (Kubendran et al., 2022; Singh et al., 2022; Kannan et al., 2022). Pulse width modulation (PWM) techniques are the most popular

- This is an Open Access article distributed under the terms of the Creative Commons Attribution-Noncommercial 4.0 Unported License, permitting all non-commercial use, distribution, and reproduction in any medium, provided the original work is properly cited.

- Selection and peer-review under responsibility of the Organizing Committee of the Conference

© 2025 Published by ISRES Publishing: www.isres.org

techniques employed for controlling the MLI (Juárez-Abad et al., 2021). Different PWM techniques have been developed, such as the sinusoidal PWM (S-PWM) technique and space vector PWM (SV-PWM) technique (Can et al., 2022; Dordevic et al., 2013). Nevertheless, both S-PWM and SV-PWM techniques (Ren et al., 2021) operate with high switching frequencies, resulting in increases the switching losses. The switching losses can be reduced by using a selective harmonic elimination -pulse width modulation (SHE-PWM) technique (Hiendro et al., 2020; Dahidah et al., 2008). Compared with S-PWM and SV-PWM techniques, SHE-PWM is a more effective MLIs control technique to achieve better harmonic performance even at low switching frequencies. The main advantage of SHE-PWM technique is the adaptation of suitable switching angles in multilevel inverters to get rid of or lower some low-order selective harmonics while maintaining the fundamental harmonic at a desired value. However, the main challenge in SHE-PWM technique is to find the set of solutions of non-linear equations that define the optimal switching angles (M. Bounabi et al., 2018). In the research literature, the techniques used to address this problem can be classified into three categories, algebraic methods (e.g., Walsh method (Vicente et al., 2011), Groebner Bases (Yang et al., 2015), etc.), numerical methods (e.g., homotopy algorithm (Hosseini Aghdam et al., 2013), Newton Raphson method (Kato et al., 1999), etc.) and metaheuristics-based algorithms (e.g., genetic algorithm (Ali et al., 2021), particle swarm optimization (PSO) (Jiang et al., 2022), etc.) as summarized in Figure. 1.

In this work, a selective harmonic elimination PWM (SHE-PWM) technique using a teaching learning based optimization (TLBO) algorithm is proposed to obtain optimal switching angles to minimize the output voltage total harmonic distortion (THD) of a 17-level single-phase cascaded H-bridge multi-level inverter (CHB-MLI). The main objective is to demonstrate the effectiveness of the (TLBO) algorithm in addressing selective harmonic elimination (SHE) and total harmonic distortion (THD) minimization in multi-level inverters (MLIs) by optimally determining the switching angles. The main contributions of the present work can be summarized as follows:

- An effective teaching learning based optimization (TLBO) meta-heuristic algorithm is adopted to solve the non-linear (SHE-PWM) problem.
- The TLBO algorithm is implemented to find the optimal switching angles that effectively reduce the THD and meet the IEEE 519-2014 standard.
- The proposed technique is applied on 17-level (CHB) inverters for varying modulation index values.
- Comparison of the performance of the proposed method with conventional modulation methods (EP, HEP, FF and HH methods) is carried out to assess its superiority.

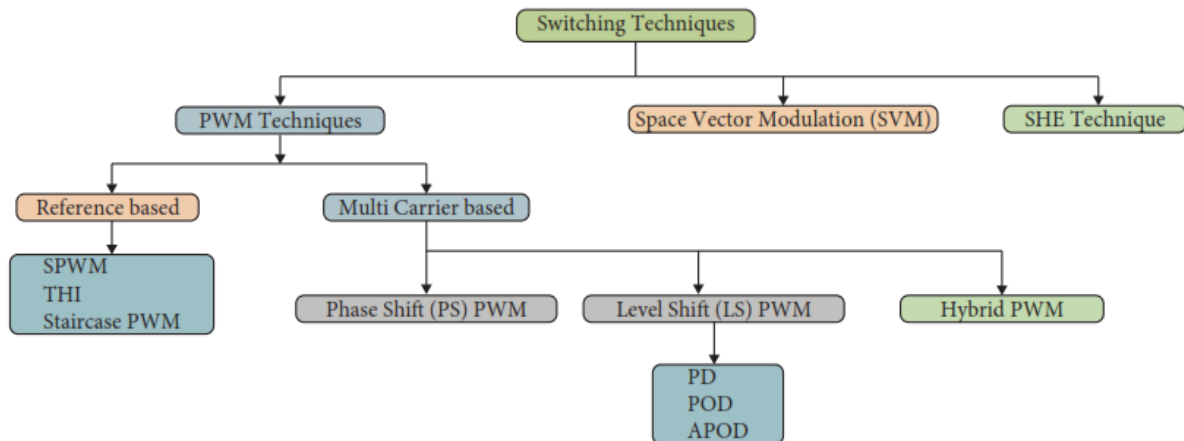


Figure 1. Classification of switching techniques (Dekka et al., 2020)

Methodology

This section outlines the application of the Teaching Learning based Optimization (TLBO) algorithm to solve the selective harmonic elimination-pulse width modulation (SHE-PWM) problem. More precisely, the solution of a set of non-linear equations and finding the optimal switching angles for eliminating the selected lower order harmonics and reducing the total harmonic distortion (THD) from the cascaded H-bridge multilevel inverter (CHB-MLI) output. The simulations of single-phase 17-level CHB inverter will be done according to the switching angles obtained from the TLBO algorithm. Additionally, the THD values were compared with those reported in the available literature obtained using different conventional modulation techniques.

Single-Phase Eleven-Level CHB-MLI

In this section, Selective Harmonic Elimination (SHE) strategy is used to produce the switching pulses using optimal switching angles for a single-phase cascaded H-bridge multilevel inverter (CHB-MLI) having the ability to produce a 17-level output voltage. Figure. 2 shows the structural design for single-phase 17-level CHB-MLI. This structure consists of 8 individual H-bridge cells connected with each other in a cascaded configuration. Each of these H-bridge cells has 4 switches and 1 DC supply, making a total of 32 switches and 8 DC supplies. All the DC supplies are equal in magnitude, making it a symmetrical configuration. The combination of all the H-bridge cells is generating the 17-level desired output voltage. Figure. 3 illustrates the staircase output voltage of the 17-level CHB-MLI for the quarter waveform of voltage cycle. Through the Figure, it can be seen that each edge of each stair represents one switching angle. These switching angles are considered as keys to eliminate selective harmonics. The switching angle θ_1 is used to control the fundamental component of the output voltage while all remaining switching angles (θ_2 to θ_8) are used to remove the harmonics. Generally, this SHE strategy is conducted by decoding the pulse width modulation (PWM) waveform utilizing Fourier analysis. Fourier series of periodic functions can be expressed as follows:

$$f(t) = \frac{A_0}{2} + \sum_{n=1}^{\infty} [A_n \cos(n\omega t) + V_n \sin(n\omega t)] \quad (1)$$

In this case $f(t) = \text{odd}$, so equation (1) can be rewritten as follows:

$$f(t) = \sum_{n=1}^{\infty} V_n \sin(n\omega t) \quad (2)$$

Here, the voltage component V_n can be determined via the following equation:

$$V_n = \frac{4V_{dc}}{n\pi} \sum_{i=1}^i \cos(n\theta_i) \quad (3)$$

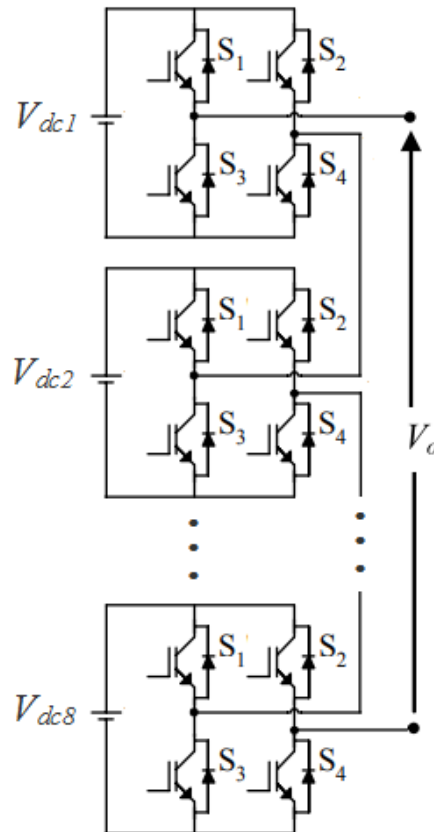


Figure 2. Structural design for single-phase 17-level (CHB-MLI)

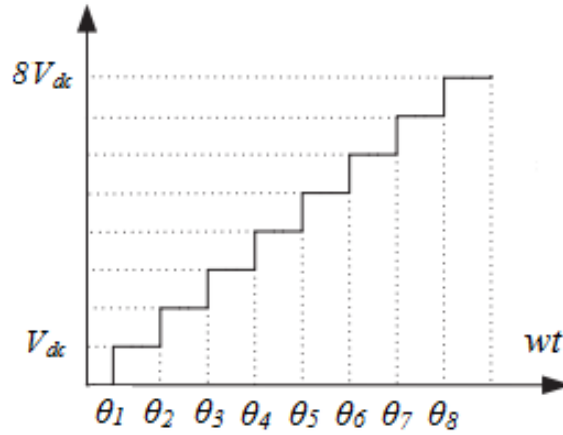


Figure 3. Quarter waveform of a 17-level inverter

Equation (4) provides the formula for the (SHE) equations for the 17-level inverter. A non-linear set of equations is used to eliminate the specific harmonics (5th, 7th, 11th, 13th, 17th, 19th, and 23th) from the output voltage. This non-linear equation set is achieved from (4) by equating V_5 , V_7 , V_{11} , V_{13} , V_{17} , V_{19} , and V_{23} to zero as follows:

$$\begin{aligned}
 V_1 &= \frac{4V_{dc}}{\pi} [\cos(\theta_1) + \cos(\theta_2) + \dots + \cos(\theta_8)] = M \\
 V_5 &= \frac{4V_{dc}}{5\pi} [\cos(5\theta_1) + \cos(5\theta_2) + \dots + \cos(5\theta_8)] = 0 \\
 V_7 &= \frac{4V_{dc}}{7\pi} [\cos(7\theta_1) + \cos(7\theta_2) + \dots + \cos(7\theta_8)] = 0 \\
 V_{11} &= \frac{4V_{dc}}{11\pi} [\cos(11\theta_1) + \cos(11\theta_2) + \dots + \cos(11\theta_8)] = 0 \\
 V_{13} &= \frac{4V_{dc}}{13\pi} [\cos(13\theta_1) + \cos(13\theta_2) + \dots + \cos(13\theta_8)] = 0 \\
 V_{17} &= \frac{4V_{dc}}{17\pi} [\cos(17\theta_1) + \cos(17\theta_2) + \dots + \cos(17\theta_8)] = 0 \\
 V_{19} &= \frac{4V_{dc}}{19\pi} [\cos(19\theta_1) + \cos(19\theta_2) + \dots + \cos(19\theta_8)] = 0 \\
 V_{23} &= \frac{4V_{dc}}{23\pi} [\cos(23\theta_1) + \cos(23\theta_2) + \dots + \cos(23\theta_8)] = 0
 \end{aligned} \tag{4}$$

According to the (SHE) strategy. With N switching angles in a quarter-cycle, there are N-1 harmonic components that can be eliminated. To eliminate 5th, 7th, 11th, 13th, 17th, 19th, and 23th order harmonics from the output voltage waveform. The equation (4) must be solved in such a way that the condition of ($0 < \theta_1 < \theta_2 < \dots < \theta_8 < 90^\circ$) is satisfied, where θ_1 to θ_8 represent switching angles. The optimum switching angles are calculated by utilizing an objective function. The TLBO algorithm finds the optimal solution using an objective function. This objective function is formulated as follows.

$$\begin{aligned}
 F(\theta_1, \theta_2, \theta_3, \dots, \theta_8) &= \left(\sum_{i=1}^8 \cos \theta_i - M \right) + \left(\frac{4}{5\pi} \sum_{i=1}^8 \cos(5\theta_i) \right) + \left(\frac{4}{7\pi} \sum_{i=1}^8 \cos(7\theta_i) \right) + \left(\frac{4}{11\pi} \sum_{i=1}^8 \cos(11\theta_i) \right) \\
 &+ \left(\frac{4}{13\pi} \sum_{i=1}^8 \cos(13\theta_i) \right) + \left(\frac{4}{17\pi} \sum_{i=1}^8 \cos(17\theta_i) \right) + \left(\frac{4}{19\pi} \sum_{i=1}^8 \cos(19\theta_i) \right) + \left(\frac{4}{23\pi} \sum_{i=1}^8 \cos(23\theta_i) \right)
 \end{aligned} \tag{5}$$

The ratio of the THD% is a measure of the harmonic content of an output voltage or output current. THD% of the output voltage can be expressed as the following:

$$THD\% = 100 * \sqrt{\sum_{n=5,7,11,13,\dots}^{\infty} \left(\frac{V_n}{V_1} \right)^2} \quad (6)$$

Overview of the Teaching Learning Based Optimization (TLBO) Algorithm

This optimization technique is an inhabited meta-heuristic technique that optimizes a given objective function (R. Venkata Rao et al., 2012). The TLBO algorithm exhibits two modes of learning: teacher-led learning and learner-led learning (referred to as the learner phase). The proposed TLBO algorithm considers the number of students in a class and the required number of iterations or generations. The teacher selects the best solution (learner), which is then reviewed by the other learners. The resulting solution is the most suitable one, requiring minimal data for implementation. The basic steps of the TLBO algorithm are given below:

Step 1: Initialize the parameters of TLBO algorithm: including the number of design parameters (D_n), the convergence rate and the sample size (P_n).

Step 2: Create a random population according to population size (n) and dimension.

Step 3: The optimization, during this phase, where the function $f(x)$ that is to be minimized is defined subject to $x_i = 1, 2, \dots$, where i is the iteration size.

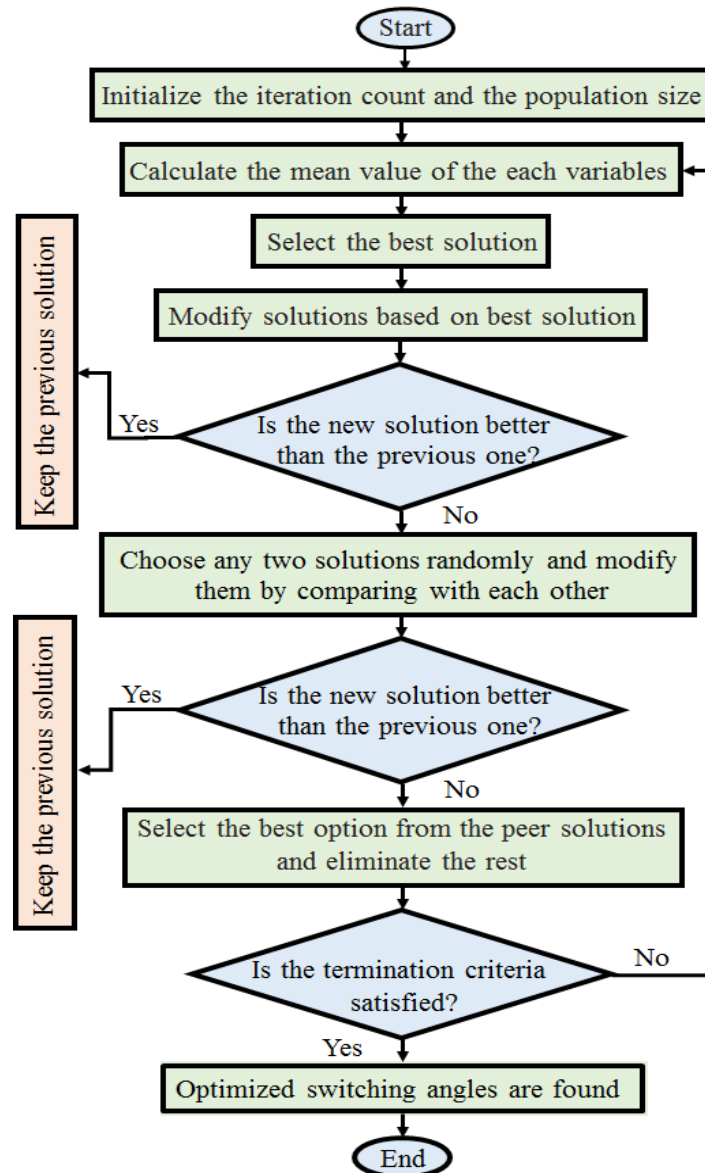


Figure 4. TLBO flowchart for solving SHE-PWM problem

Step 4: Find the mean of parameters by using an arbitrary population of the same size.

Step 5: The teacher phase, during this phase, the least fit student is the teacher, or the best solution X_{new} is employed, where X_{mean} is the mean of all the students in the class. The equation to generate a new solution is given by:

$$X_{new} = X + r.(X_{best} - T_f.X_{mean}) \quad (7)$$

where X_{best} represents the teacher, X_{new} represents the new selection, X represents the current solution, X_{mean} represents the mean of the solution, r represents a random number in the range (0, 1) and T_f represents the teaching factor and is either 1 or 2, the value of T_f is decided randomly.

Step 6: The learner phase, during this phase, learners gain information by interacting among themselves, the generate a new solution of this phase is expressed below:

$$X_{new} = X + r.(X - X_p) \quad (8)$$

where X_p is the partner solution, X represents the current solution and r represents a random number in the range (0, 1).

Step 7: Termination criteria. Termination criteria stop if the maximum generation is achieved; otherwise repeat from Step 5 and continue until all of the closure conditions are met. Detail flow chart of TLBO is shown in Figure 4.

Results and Discussion

In this section, a single-phase CHB-MLI to show the efficacy and robust of TLBO algorithm in solving SHE optimization problem. The inverter is a 17-level inverter, therefore 8 optimal angles are specified by the TLBO algorithm. The 17-level inverter with 315V output voltage and 50 Hz. The parameters of TLBO algorithm used for simulation are listed in Table (1).

Table 1. Simulation and optimization parameters

N ^o	Parameters	Values
1	Population Size	100
2	Number of Iterations	500
3	Number of dimensions	10
4	Lower Boundary	[0°, 0°, 0°, 0°, 0°, 0°, 0°, 0°]
5	Upper Boundary	[90°, 90°, 90°, 90°, 90°, 90°, 90°, 90°]
6	Modulation index	0 < MI ≤ 1

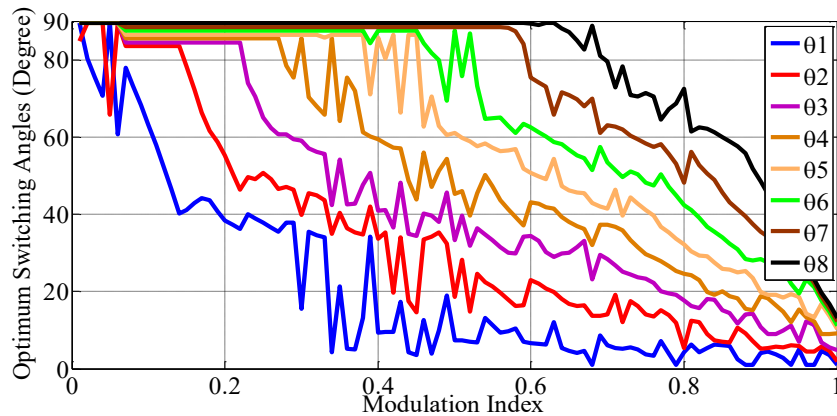


Figure 5. Optimum switching angle trend with modulation index obtained from TLBO algorithm

Figure. 5, shows optimal switching angles obtained using TLBO algorithm against different values of modulation index (MI). From the given Figure 5, it is evident that the value of the optimum solution set, comprising different switching angles (θ_1 to θ_8), converges to a lower value with the increase in the value of the (MI). The switching angles calculated by TLBO, THD% and fitness values versus different modulation indices for 17-level MLI are shown in Table 2.

Table 2. Simulation and optimization parameters			
Switching Angle	MI	MI	MI
	0.2	0.6	1
θ_1	38.38	6.67	1.90
θ_2	55.32	22.91	1.99
θ_3	84.55	34.33	4.86
θ_4	85.56	43.16	9.19
θ_5	87.54	50.67	10.99
θ_6	88.54	62.45	11.30
θ_7	89.50	75.35	12.40
θ_8	90	89.45	13.20

From Table 2, it is observed that the TLBO algorithm successfully solved the SHE equations in the range of 0.1–1 modulation index (MI) and interesting thing is that THD% produced during range 0.5–0.95 of MI are less than 8% and minimum THD% of value 4.31%. Figure. 6 shows the objective function value of TLBO algorithm versus modulation index. The Figure indicates that TLBO algorithms were successful in finding solutions across the entire modulation index range ($0.1 \leq MI \leq 1$). Moreover, TLBO algorithm produced the optimum fitness value of $6.75e^{-13}$, which demonstrates its stability and superior performance in solve the non-linear SHE-PWM problem

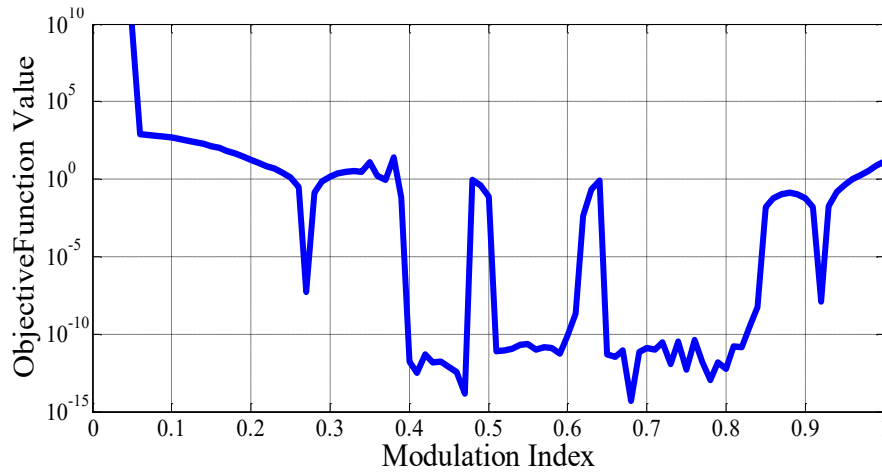


Figure 6. Objective function value for TLBO algorithm versus modulation index

Figure. 7 shows harmonics based on modulation index from 0.1 to 1. Figure. 7 indicates that the low order harmonics are eliminated 5th, 7th, 11th, 13th, 17th, 19th and 23th within the range of (0.5–0.95) modulation index. Figure. 8 represents the THD% of output voltage at various modulation index (MI). It can be seen that the THD% of output voltage value in the range of (0.5–0.95) modulation index, is found to be less than 8.0% and meets the IEEE 519-2014 standard and minimum THD% of value 4.31% has occurred.

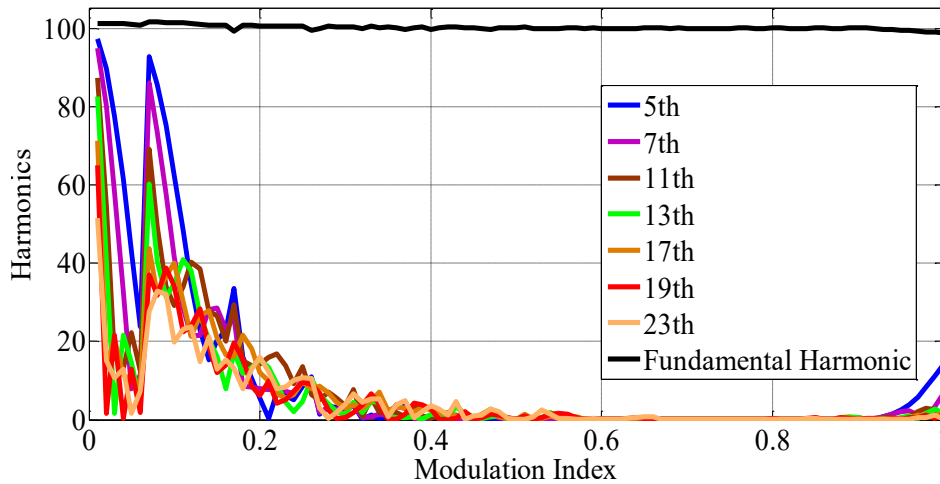


Figure 7. Harmonic trend with respect to the modulation index

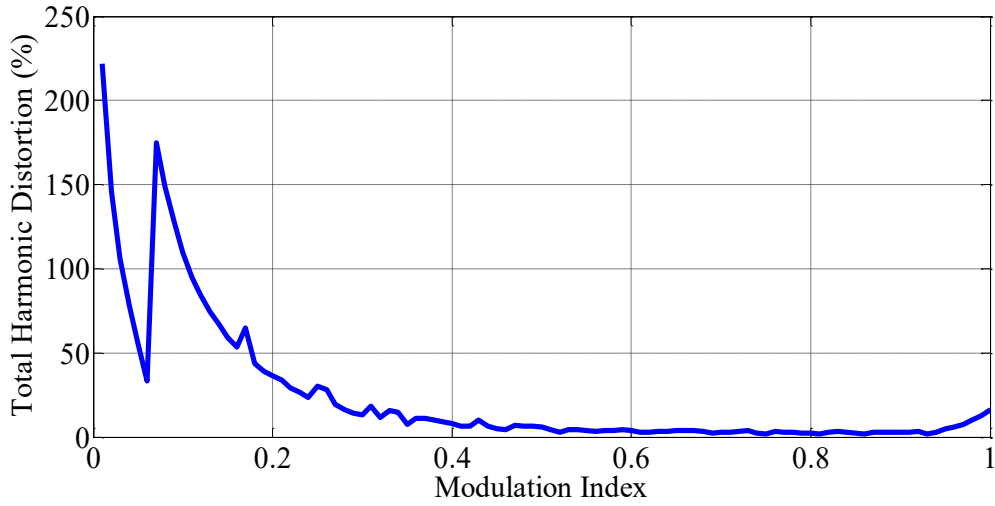


Figure 8. THD % Vs modulation index

Figure. 9 shows the magnitude of fundamental harmonic versus modulation index (MI). It can be observed the maximum deviation is within 1.3%. Figure. 10 depict the stepped output voltage waveform of 17-level a CHB-MLI with 0.8 modulation indexes using the TLBO. Fast Fourier transform (FFT) analysis at modulation index (MI) of value 0.8 is presented in Figure. 11. It reveals that the targeted lower order harmonics of 5th, 7th, 11th, 13th, 17th, 19th and 23th are eliminated and the THD% is 4.31%, which complies with IEEE 519-2014 harmonic guidelines.

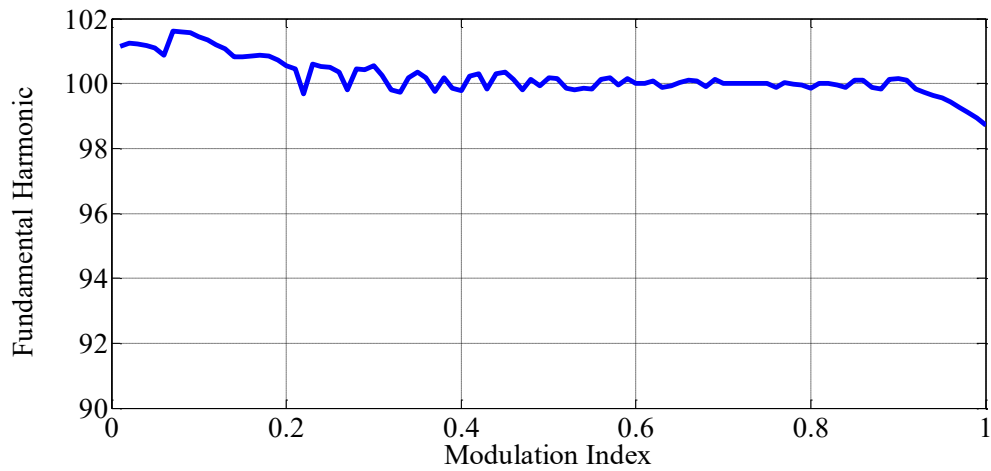


Figure 9. The calculated fundamental harmonic to V1

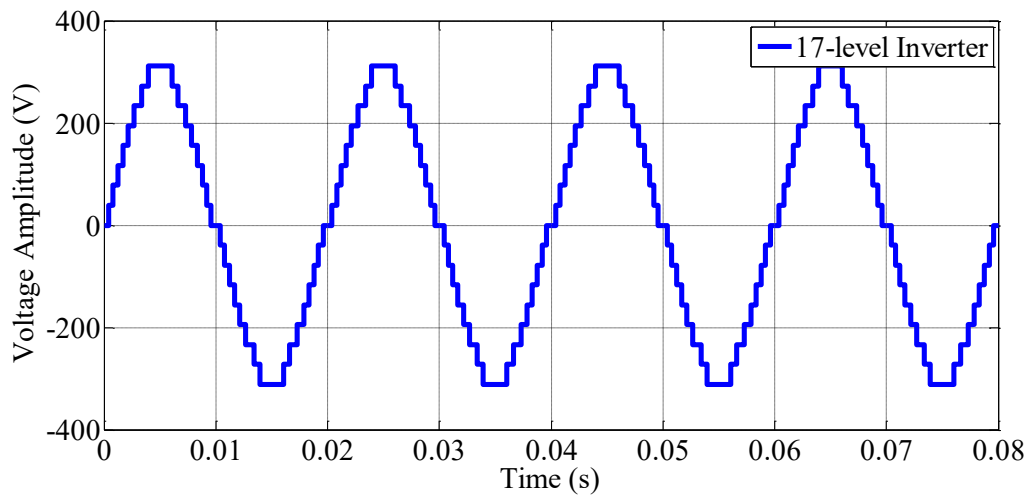


Figure 10. Simulation result of output voltage waveform for 17-level CHB-MLI at modulation index 0.8

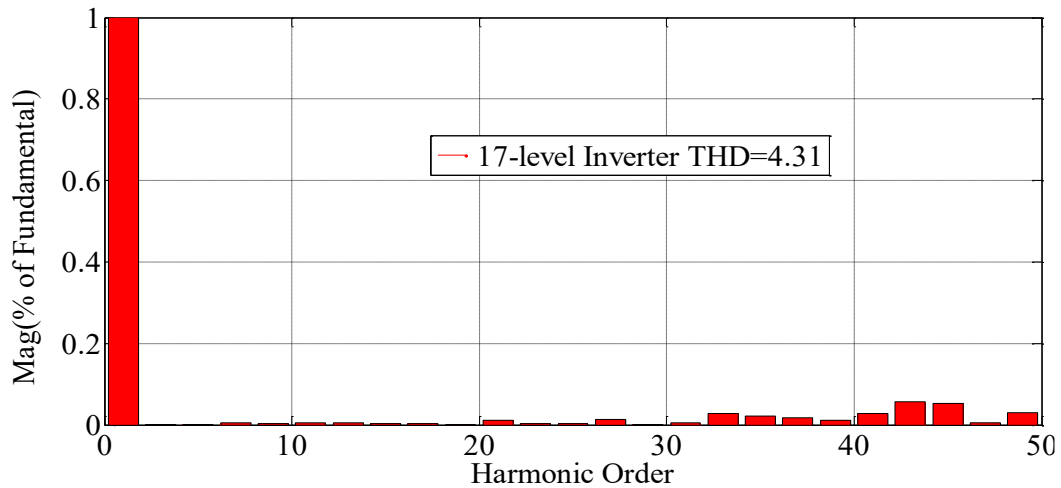


Figure 11. FFT analysis representing order of harmonics in the output voltage.

Table 3 summarizes the percentage of THD by EP, HEP, FF, HH and TLBO methods for voltage levels of 17. Noted that the best system performance is by TLBO method, in which the least THD obtained is 4.31%. This is because of better suppression of lower order harmonic spectrum by TLBO method.

Table 3. Comparative analysis of THD% at 17- level

Modulation technique	%THD
EP (Taha Abdulsalam Taha et al., 2025)	17.55%
HEP (Taha Abdulsalam Taha et al., 2025)	16.35%
FF (Taha Abdulsalam Taha et al., 2025)	20.90 %
HH (Taha Abdulsalam Taha et al., 2025)	5.02%
Proposed TLBO algorithm	4.31%

Conclusion

This paper focused on using the teaching learning-based optimization (TLBO) algorithm with selective harmonic elimination-pulse width modulation (SHE-PWM) technique to reduce total harmonic distortion (THD) on the phase voltage in a multilevel inverter (MLI) topology. The TLBO algorithm has been implemented to find the optimal switching angles of a single-phase 17-level cascaded H-bridge MLI (CHB-MLI). According to simulation results, the proposed method successfully solves non-linear equations for SHE, achieving a reduced THD of 4.31% while eliminating 5th, 7th, 11th, 13th, 17th, 19th and 23rd harmonics. Furthermore, the THD values were compared with those reported in the existing literature obtained using different conventional modulation methods. The results indicated that the THD obtained using the TLBO algorithm was lower compared to the equal phase (EP), half equal phase (HEP), feed forward (FF) and half height (HH) methods. The proposed strategy can be further extended to reduce higher order harmonics. In addition, it can be implemented to other topologies.

Scientific Ethics Declaration

* The authors declare that the scientific ethical and legal responsibility of this article published in EPSTEM journal belongs to the authors.

Conflict of Interest

* The authors declare that they have no conflicts of interest

Funding

* This research received no specific grant from any funding agency in the public, commercial, or not-for-profit sectors.

Acknowledgements or Notes

* This article was presented as an oral presentation at the International Conference on Technology, Engineering and Science (www.icontes.net) held in Antalya/Türkiye on November 12-15, 2025.

References

- Adam, G. P., Finney, S. J., Ojo, O., & Williams, B. W. (2012). Quasi-two-level and three-level operation of a diode-clamped multilevel inverter using space vector modulation. *IET Power Electronics*, 5(5), 542-551.
- Ali, M., Mehmood, K. K., Baloch, S., & Kim, C. H. (2021). Robust ANN-based control of modified puc-5 inverter for solar PV applications. *IEEE Transactions on Industry Applications*, 57(4), 3863-3876.
- Behera, R. R., Dash, A. R., & Panda, A. K. (2022). A novel cascaded transformer coupled multilevel inverter with reduced number of switches for high power applications. *World Journal of Engineering*, 20(6), 1025-1044.
- Bounabi, M., Kaced, K., Ait-Cheikh, M. S., Larbes, C., Dahmane, Z., & Ramzan, N. (2018). Modelling and performance analysis of different multilevel inverter topologies using PSO-MPPT technique for grid connected photovoltaic systems. *Journal of Renewable and Sustainable Energy*, 10(4), 043507.
- Can, E., & Sayan, H. H. (2022). Development of fractional sinus pulse width modulation with β gap on three step signal processing. *International Journal of Electronics*, 110(3), 527-546.
- Chunyan, Z., & Zhao, L. (2015). Advanced compensation mode for cascade multilevel static synchronous compensator under unbalanced voltage. *IET Power Electronics*, 8(4), 610-617.
- Dahidah, M. S. A., & Agelidis, V. G. (2008). Selective harmonic elimination PWM control for cascaded multilevel voltage source converters: A generalized formula. *IEEE Transactions on Power Electronics*, 23(4), 1620-1630.
- Dekka, A., Ramezani, A., Ouni, S., & Narimani, M. (2020). A new five-level voltage source inverter: Modulation and control. *IEEE Transactions on Industry Applications*, 56(5), 5553-5564.
- Dordevic, O., Jones, M., & Levi, E. (2013). A comparison of carrier-based and space vector PWM techniques for three-level five-phase voltage source inverters. *IEEE Transactions on Industrial Informatics*, 9(2), 609-619.
- Elias, M. F. M., Rahim, N. A., & Rosli, N. F. (2023). A three-phase hybrid multilevel inverter with enhanced pulse-width modulation strategy. *IEEE Transactions on Power Electronics*, 38(4), 4714-4726.
- Hiendro, A., Yusuf, I., Junaidi, J., Wigyananto, T. P., & Simanjuntak, Y. M. (2020). Optimization of SHEPWM cascaded multi-level inverter switching patterns. *International Journal of Power Electronics and Drive Systems*, 11(3), 1570-1578.
- Hosseini Aghdam, G. (2013). Optimised active harmonic elimination technique for three-level t-type inverters. *IET Power Electronics*, 6(3), 425-433.
- Jiang, Y., Li, X., Qin, C., Xing, X., & Chen, Z. (2022). Improved particle swarm optimization based selective harmonic elimination and neutral point balance control for three-level inverter in low-voltage ride-through operation. *IEEE Transactions on Industrial Informatics*, 18(1), 642-652.
- Juárez-Abad, J. A., Barahona-Avalos, J. L., & Linares-Flores, J. (2021). PWM techniques for an asymmetric multilevel binary inverter: An FPGA-based implementation. *IET Power Electronics*, 14(8), 1529-1539.
- Kannan, C., Priyadharsini, S., Babu, L. G., Mugilvannan, S., Thamotharan, K., & Velan, V. (2022). Design of modular and non modular multilevel inverter topology with reduced number of switches. *EPRA International Journal of Research and Development (IJRD)*, 7(6), 249-255.
- Kato, T. (1999). Sequential homotopy-based computation of multiple solutions for selected harmonic elimination in PWM inverters. *IEEE Transactions on Circuits and Systems I: Fundamental Theory and Applications*, 46(5), 586-593.
- Kubendran, V., Mohamed Shuaib, Y., & Preetha Roselyn, J. (2022). Development of multilevel inverter with reduced switch counts and limited sources for electric vehicles. *Sustainable Energy Technologies and Assessments*, 52, 102332.
- Mehta, S., & Puri, V. A. (2022). Review of different multi-level inverter topologies for grid integration of solar photovoltaic system. *Renewable Energy Focus*, 43, 263-276.

- Meraj, S. T., Yu, S. S., Rahman, M. S., Hasan, K., Hossain Lipu, M. S., & Trinh, H. (2023). Energy management schemes, challenges and impacts of emerging inverter technology for renewable energy integration towards grid decarbonization. *Journal of Cleaner Production*, 405, 137002.
- Meynard, T., & Foch, H. (1992). Multi-level choppers for high voltage applications. *EPE Journal*, 2(1), 45-50.
- Naga Sai Charan, P., & Meenendranath Reddy, K. (2023). An analysis of multilevel converter for faster current control in a DC microgrid with extremely low-impedance interconnections. *Journal of Image Processing and Intelligent Remote Sensing*, 3, 18-29.
- Pires, V. F., Foito, D., & Silva, J. F. (2017). Fault-tolerant multilevel topology based on three-phase H-bridge inverters for open-end winding induction motor drives. *IEEE Transactions on Energy Conversion*, 32(3), 895-902.
- Rao, R. V., & Patel, V. (2012). An elitist teaching-learning-based optimization algorithm for solving complex constrained optimization problems. *International Journal of Industrial Engineering Computations*, 3(3), 535-560.
- Ren, W. (2021). *Research on modulation strategy of cascaded H-bridge multilevel inverter* (Master's thesis) East China Jiaotong University.
- Sedaghati, F., Ebrahimzadeh, S., Dolati, H., & Shayeghi, H. (2022). A modified switched capacitor multilevel inverter with symmetric and asymmetric extendable configurations. *Journal of Operation and Automation in Power Engineering*, 20, 20.
- Singh, A., Bhandari, S., & Kumar, J. (2022). (Paper presentation). *1st International Conference on Sustainable Technology for Power and Energy Systems (STPES)*, Srinagar, India.
- Sotoodeh, P., & Miller, R. D. (2013). A new multi-level inverter with facts capabilities for wind applications. In *IEEE* (pp. 271–276).
- Taha, T. A., Shalaby, M., Abdul Wahab, N. I., Zaynal, H. I., Hassan, M. K., Al-Sowayan, S., & Alawad, M. A. (2025). Recent advancements in multilevel inverters: Topologies, modulation techniques, and emerging applications. *Symmetry*, 17, 1010.
- Taiea, A., Mohamed, E. E. M., Youssef, A., Ibrahim, A. A., Saeed, M. S. R., & Ali, A. I. M. (2020). Three phase modular multilevel inverter-based multi-terminal asymmetrical DC inputs for renewable energy applications. *Engineering Science and Technology, an International Journal*, 23(4), 831–839.
- Vicente, J., Pindado, R., & Martinez, I. (2011). (Paper presentation). *7th International Conference-Workshop Compatibility and Power Electronics (CPE)*.
- Yang, K., Yuan, Z., Yuan, R., Yu, W., Yuan, J., & Wang, J. (2015). A groebner bases theory-based method for selective harmonic elimination. *IEEE Transactions on Power Electronics*, 30(12), 6581–6592.

Author(s) Information

Smail Toufik

Solar Equipements Development Unit (UDES)
 UDES, National Road N°11, BP 386, Bou-Ismaïl, 42415,
 Tipaza, Algeria
 Contact e-mail: smail.toufik77@gmail.com

Boulouma Sabri

Solar Equipements Development Unit (UDES)
 UDES, National Road N°11, BP 386, Bou-Ismaïl, 42415,
 Tipaza, Algeria

Seddaoui Naoual

Solar Equipements Development Unit (UDES)
 UDES, National Road N°11, BP 386, Bou-Ismaïl, 42415,
 Tipaza, Algeria

Rahmani Hachemi

Solar Equipements Development Unit (UDES)
 UDES, National Road N°11, BP 386, Bou-Ismaïl, 42415,
 Tipaza, Algeria

To cite this article:

Toufik, S., Sabri, B., Naoual, S., & Hachemi, R. (2025). Voltage total harmonic distortion reduction in multi-level single-phase inverters using the SHE-PWM technique with a TLBO algorithm. *The Eurasia Proceedings of Science, Technology, Engineering and Mathematics (EPSTEM)*, 38, 688-698.